

REMARKS

The Office Action of May 27, 2005 has been carefully considered. In response thereto, the claims have been amended as set forth above. Reconsideration and allowance in view of the foregoing amendments and the following remarks is respectfully requested.

Claims 1-6 were rejected as being inadequately supported in the specification by reason of the prior amendment. The present amendment is believed to render this rejection moot.

Claims 1-6 were rejected as being unpatentable over Hauck in view of Trimberger. The claims have been amended to more clearly distinguish over the cited references. Reconsideration is respectfully requested.

In particular, the claims have been amended to recite that for at least one configured function, a relative order of outputs of different logic blocks is reversed during a second routing, i.e., a routing outputs of the programmable logic blocks to bits of the result to which the programmable logic blocks are assigned

Neither Hauck nor Trimberger is believed to teach or suggest such a feature.

With respect to Hauck (Fig. 2), the output multiplexers are apparently capable of performing reordering of the outputs of a single logic block. They are not capable, however, of reversing the relative order of outputs of different logic blocks such as the left-hand and right-hand logic blocks of Hauck Fig. 2.

The importance of this reordering capability is described in the Specification, for example at page 3, lines 4-17.

Withdrawal of the rejections and allowance of claims 1-6 is respectfully requested.

Respectfully submitted,



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